



भारतीय प्रौद्योगिकी संस्थान भिलाई

जी. ई. सी. कैम्पस, सेजबहार, रायपुर – ४९२०१५
छत्तीसगढ़, भारत

Indian Institute of Technology Bhilai

GEC Campus, Sejbahar, Raipur – 492015
Chhattisgarh, India
Phone # 0771-2973600/602

Advertisement for the post of Project Assistant in a MeITY-funded Research Project

Applications are invited from Indian nationals for the position of Project Assistant in a MeITY funded research project with following details:

Title of the project: Deep Learning based Real Time Hardware in Loop Simulation for Islanding Detection and Protection in Microgrid

Principal Investigator: Dr. Prashant Agnihotri, Assistant Professor, Room 306, Department of Electrical Engineering and Computer Science, IIT Bhilai, GEC Campus, Sejbahar, Raipur, Chhattisgarh- 492015 (+91-6265074473 email: pagnihotri@iitbhilai.ac.in).

Name of the position: Project Assistant

Number of Positions: 01

Essential qualifications: Engineering graduate (Electrical Engineering/Electronics Engineering) from a recognized institute/university or statutory body with a minimum of 60% aggregate score (6.5 grade points on a scale of 10) with Minimum ONE Year of relevant work experience.

In case of no experience, the candidate must have MTech/ME/MS (Electrical Engineering/Electronics Engineering) from a recognized institute/university or statutory body with a minimum of 60% aggregate score (6.5 grade points on a scale of 10).

For SC/ST category, the candidate must have secured a minimum of 55% aggregate score (6.0 grade points on a scale of 10).

Desirable: Preference will be given to candidates NET/GATE qualified candidates and having work experience relevant to the project.

Candidates with expertise in the followings are strongly encouraged to apply:

- 1) Power System and Power Electronics with hands on simulation experience in MATLAB/PSCAD and hardware design experience.
- 2) Preferred if you have knowledge about Microgrid operation and control.
- 3) Preferred if you have experience of programming in Deep Learning using Python language. Certification in the Machine Learning and Deep Learning (e.g from Coursera) will be preferred.
- 4) Preferred if you have experience of working with real time simulator such as Opal RT/RTDS/Typhoon HIL towards application in Power Electronics and Power System.
- 5) Good communication skill and problem solving capability.

Age limit: 35 years (on the date of application)



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Salary: Consolidated salary of Rs.20,000-28,000 per month (depending upon the qualification and experience). Based on experience and performance in the interview, a higher position such as Project Associate may be offered.

Duration: Three years or up to the termination of the project, subject to annual performance review.

How to apply: Interested candidates must apply on the application form attached with this advertisement. **Duly completed application form along with candidate's detailed CV must be emailed to the PI, Dr. Prashant Agnihotri at pagnihotri@iitbhilai.ac.in by Monday, 13th July 2020. Subject of the email should read Application for Project Associate – Applicant's Name. Applications without application form will not be shortlisted.**

Only shortlisted candidates will be called for interview and no TA/DA will be paid for attending the interview. Candidates should make their own arrangements for stay. The positions are available immediately. **(In case of extension of lockdown or travel restrictions due to COVID19, online interviews may be conducted)**

Due date: Monday, 13th July. (on or before 5.00 pm) via email.

Conditions:

- No TA/DA or accommodation will be provided to the candidates called for the interview. •
- The PI shall not be responsible for email delay, if any, or any other reason for non-receipt of the document at the specified time and will result in disqualification/rejection of the application.
- If the number of applications for the position is large, the selection committee may decide an appropriate shortlisting criteria over and above the essential qualification required to restrict the number of candidates for the interview to a reasonable limit. •
- The decision of the selection committee will be final.